<table>
<thead>
<tr>
<th>Position Title</th>
<th>PhD Researcher in Digitally Assisted Analog Circuits - development of digital circuit blocks to improve the performance of high-speed RF D/A converters</th>
</tr>
</thead>
<tbody>
<tr>
<td>Project</td>
<td>The Circuits and Systems Research Centre (CSRC: <a href="http://www.csrc.ie">www.csrc.ie</a>) at the University of Limerick, Ireland is a partner in the SFI CONNECT centre (<a href="http://www.connectcentre.ie">www.connectcentre.ie</a>) for Internet of Things and has a 4-year funded vacancy for a PhD researcher. The CSRC is a leading centre for microelectronics research with activities in data converters, power management and signal processing. The team is involved in a number of projects collaborating with industrial and research partners to develop innovative solutions in a variety of applications areas. In this role, the researcher will be responsible for the development of digital circuit blocks to improve the performance of high-speed RF D/A converters. The researcher should have an interest in pursuing a PhD in the area of digital signal processing techniques to improve the performance of D/A converters. Role and Responsibilities:</td>
</tr>
<tr>
<td>Faculty</td>
<td>Science and Engineering</td>
</tr>
</tbody>
</table>
**Entry Requirements**

A minimum 2.1 honours degree in Electronic Engineering or MEng/MSC qualification with a VLSI background is necessary.

The candidate will also meet the following requirements;

- Strong background in digital/mixed-signal design and signal processing.
- Excellent communication skills.
- Working knowledge of MATLAB and modeling tools such as spice simulators.
- Proficient use of digital RTL and Industry standard IC design tools.

**Funding/Stipend**

The funded project will cover student fees and offer an attractive stipend.

**Funding Notes**

This research is conducted with the financial support of Science Foundation Ireland (SFI) under Grant Number 13/RC/2077 and has been part funded by the European Regional Development Fund through the SFI Research Centres Programme.

**Closing Date**

30th June, 2017

**Contact**

Dr. Brendan Mullane & Dr. Tony Scanlan

[Brendan.Mullane@ul.ie](mailto:Brendan.Mullane@ul.ie) & [Tony.Scanlan@ul.ie](mailto:Tony.Scanlan@ul.ie)

See [http://ulsites.ul.ie/csdc/vacancies-2](http://ulsites.ul.ie/csdc/vacancies-2)

**Additional Information**

**Preferred Skills:**

Strong consideration will be given to candidates that also meet the following requirements;

- Signal processing algorithm development, particularly for data converter error correction.
- A good knowledge of FPGA design tools.
- Industrial expertise working in a team environment and project examples that showcase these skill requirements.

**Application:** Please send your CV/Resume and a cover letter in support of these requirements to [Brendan.Mullane@ul.ie](mailto:Brendan.Mullane@ul.ie) and [Tony.Scanlan@ul.ie](mailto:Tony.Scanlan@ul.ie)